

Appl. No. 10/810,024
Amdt. Dated July 25, 2006
Reply to Office Action of May 15, 2006

Amendments to the Claims:

This listing of claims will replace all prior versions and listings of claims in the application:

Listing of Claims:

Claim 1 (currently amended): A method for making a field emission display comprising the following steps:

providing a substrate;

forming cathode electrodes on the substrate, the cathode electrodes together with the substrate defining a pixel pattern;

forming a barrier array;

forming gate electrodes on the barrier array, the barrier array comprising a shadow mask and an insulative layer formed on the shadow mask, the shadow mask defining a plurality of openings according to the pixel pattern of the field emission display;

fixing the barrier array with the gate electrodes to the substrate; and

packaging a phosphor screen with the substrate[[:]].

~~wherein the barrier array is formed by depositing an insulative layer on a shadow mask which defines a plurality of openings according to the pixel pattern of the field emission display.~~

Claim 2 (original): The method for making a field emission display as described in claim 1, wherein the substrate can be glass, ceramic, silicon oxide, alumina or another suitable insulative material having a surface with a total thickness variation less than 1 micrometer.

Appl. No. 10/810,024
Amdt. Dated July 25, 2006
Reply to Office Action of May 15, 2006

Claim 3 (original): The method for making a field emission display as described in claim 1, wherein the method further comprises the step of providing a mask having a pattern according to the pixel pattern.

Claim 4 (original): The method for making a field emission display as described in claim 1, wherein the shadow mask is made from a material selected from the group: invar, low carbon steel, or another suitable metal alloy, and the material has a coefficient of thermal expansion matching that of the substrate.

Claim 5 (original): The method for making a field emission display as described in claim 1, wherein the insulative layer comprises alumina or magnesia.

Claim 6 (original): The method for making a field emission display as described in claim 5, wherein a thickness of the insulative layer is in the range from 10 to 500 micrometers.

Claim 7 (original): The method for making a field emission display as described in claim 5, wherein the insulative layer is formed on the shadow mask by spray coating.

Claim 8 (original): The method for making a field emission display as described in claim 5, wherein the insulative layer is formed on the shadow mask by electrophoretic deposition.

Claims 9 (original): The method for making a field emission display as described in claim 8, wherein, after the insulative layer has been deposited on the shadow mask, the barrier array is preferably soaked in a solution for a

Appl. No. 10/810,024
Amtd. Dated July 25, 2006
Reply to Office Action of May 15, 2006

predetermined time to clean surfaces of the barrier array.

Claims 10 (original): The method for making a field emission display as described in claim 1, wherein the gate electrodes are formed on the barrier array by electron beam evaporation, thermal evaporation or sputtering.

Claims 11 (original): The method for making a field emission display as described in claim 1, wherein the method further comprises a step of attaching the barrier array to a frame having a fixing surface.

Claims 12 (original): The method for making a field emission display as described in claim 1, wherein the barrier array with the gate electrodes formed thereon is fixed to the substrate by means selected from fusing powdered glass having a low melting point, welding or employing suitable clips.

Claims 13 (original): The method for making a field emission display as described in claim 1, wherein emitters employed by the field emission display comprise carbon nanotubes or metal microtips.

Claim 14 (original): A method for making a field emission display comprising the following steps:

- providing a substrate;
- forming cathode electrodes on the substrate, the cathode electrodes together with the substrate defining a pixel pattern;
- providing a metal plate defining a plurality of openings according to the pixel pattern with an insulative layer formed thereon;
- forming gate electrodes on the insulative layer;
- fixing the metal plate with the insulative layer formed thereon to the

Appl. No. 10/810,024
Amdt. Dated July 25, 2006
Reply to Office Action of May 15, 2006

substrate; and
packaging a phosphor screen with the substrate.

Claims 15 (original): The method for making a field emission display as described in claim 14, wherein emitters employed by the field emission display comprise carbon nanotubes or metal microtips.

Claim 16 (original): The method for making a field emission display as described in claim 14, wherein the metal plate is selected from the group: invar, low carbon steel, or other suitable metal alloys, and the metal plate has a thermal expansion coefficient matching that of the substrate.

Claims 17 (original): The method for making a field emission display as described in claim 14, wherein the metal plate with the insulative layer formed thereon is fixed to the substrate by means selected from fusing powdered glass having a low melting point, welding or employing suitable clips.

Claims 18 (canceled):